



Attorney Docket No.: 0553-0401

JW

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)
Yoshifumi TANADA)
Serial No.: 10/807,692)
Filed: March 24, 2004)
For: Circuit For Inspecting Semiconductor)
Device And Inspecting Method)
Examiner: Vibol Tan)
Art Unit: 2819)

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to:
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June 13 2006
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Shannon Wallace
Name of applicant, assignee, or Registered Rep.

Shannon Wallace 6/13/06
Signature Date

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT E

In response to Office Action of March 14, 2006, please amend the above-identified application as follows: